

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (original) A method comprising:

serially transmitting and receiving signals complying with the standard Universal Serial Bus (USB) specification over a communication path; and

serially transmitting and receiving signals at a frequency higher than the signals complying with the standard USB specification over the communication path.

2. (original) The method of claim 1, wherein at least one of serially transmitting and receiving signals at a frequency higher than the signals complying with the standard USB specification comprises:

serially transmitting and receiving signals at a rate of at least 125 megabits per second.

3. (original) The method of claim 1, wherein serially transmitting and receiving signals complying with the standard USB specification comprises:

serially transmitting and receiving signals at a rate of about 1.5 megabits per second or about 12 megabits per second.

4. (original) The method of claim 1, further comprising:

serially transmitting and receiving signals complying with the standard USB specification over at least one pin.

5. (original) The method of claim 1, further comprising:

configuring a transceiver to serially transmit and receive the higher frequency signals.

6. (original) The method of claim 5, further comprising:

serially transmitting and receiving signals complying with the standard USB specification over a cable.

7. (original) A system comprising:

a transceiver and a communication path;

the transceiver capable of serially transmitting and receiving signals complying with the standard Universal Serial Bus (USB) specification over the communication path;

the transceiver further being capable of serially transmitting and receiving signals at a frequency higher than the signals complying with the standard USB specification over the communication path.

8. (original) The system of claim 7, further comprising:

the transceiver further being capable of configuring itself to transmit and receive the higher frequency signals.

9. (original) The system of claim 8, wherein the communication path comprises at least one cable or pin.

10. (original) A system comprising:

a downstream transceiver; an upstream transceiver; and a cable coupling the upstream and downstream transceivers;

the transceivers capable of transmitting and receiving signals complying with the standard Universal Serial Bus (USB) specification over the cable;

at least one of the transceivers further being capable of transmitting and receiving signals at a frequency higher than the signals complying with the standard USB specification over the cable;

at least one of the transceivers capable of configuring itself to transmit and receive the higher frequency signals.

11. (original) The system of claim 10, wherein the upstream transceiver is coupled to transmit and receive signals for a host computer.

12. (original) The system of claim 10, wherein the downstream transceiver is coupled to transmit and receive signals for a peripheral.

13. (original) The system of claim 10, wherein the higher frequency signals have a rate of at least 125 megabits per second.

14. (original) The system of claim 10, wherein the signals complying with the standard USB specification have a rate of 1.5 megabits per second or 12 megabits per second.

15. (original) A system comprising:

at least two transceivers; and a communication path coupling the transceivers;

at least one of the transceivers capable of transmitting and receiving signals complying with the standard Universal Serial Bus (USB) specification over the communication path;

at least one of the transceivers capable of transmitting and receiving signals at a frequency higher than the signals complying with the standard USB specification over the communication path;

at least one of the transceivers capable of configuring itself to transmit and receive the higher frequency signals.

16. (original) The system of claim 15, wherein the higher frequency signals have a rate of at least 125 megabits per second.

17. (original) The system of claim 15, wherein the signals complying with the standard USB specification have a rate of 1.5 megabits per second or 12 megabits per second.

18. (original) An apparatus comprising:

circuitry to serially transmit and receive signals complying with the standard

Universal Serial Bus (USB) specification over a communication path; and

circuitry to serially transmit and receive signals at a frequency higher than the signals complying with the standard USB specification over the communication path.

19. (original) The apparatus of claim 18, further comprising circuitry to configure the circuitry to serially transmit and receive the higher frequency signals.

20. (original) The apparatus of claim 18, wherein the circuitry to serially transmit and receive the higher frequency signals shares at least some circuitry with the circuitry to transmit and receive signals complying with the standard USB specification.

21. (original) The apparatus of claim 18, wherein the communication path comprises at least one cable or pin.

22. (original) An integrated circuit comprising:

circuitry to serially transmit and receive signals complying with the standard Universal Serial Bus (USB) specification over a communication path;

circuitry to serially transmit and receive signals at a frequency higher than the signals complying with the standard USB specification over the communication path;
and

circuitry to configure the circuitry to serially transmit and receive the higher frequency signals.

23. (original) The integrated circuit of claim 22, wherein the circuitry to serially transmit and receive signals complying with the standard Universal Serial Bus (USB) specification comprises voltage driven circuitry.

24. (original) The integrated circuit of claim 22, wherein the circuitry to serially transmit and receive signals at a frequency higher than the signals complying with the standard USB comprises current driven circuitry.

25. (original) The integrated circuit of claim 22, wherein the circuitry to configure the circuitry to serially transmit and receive the higher frequency signals comprises a switch and a resistor.

26. (original) The integrated circuit of claim 22, wherein the circuitry to configure the circuitry to serially transmit and receive the higher frequency signals comprises a current source.

27. (original) A computer peripheral comprising:

circuitry to serially transmit and receive signals complying with the standard Universal Serial Bus (USB) specification over a communication path;

current driven circuitry to serially transmit and receive signals at a frequency higher than the signals complying with the standard USB specification over the communication path; and

circuitry to configure the current driven circuitry to serially transmit and receive the higher frequency signals.

28. (original) The computer peripheral of claim 27, wherein the circuitry to serially transmit and receive signals complying with the standard Universal Serial Bus (USB) specification comprises voltage driven circuitry.

29. (original) The computer peripheral of claim 27, wherein the circuitry to configure comprises a switch and a resistor.

30. (original) The computer peripheral of claim 27, wherein the circuitry to configure comprises a current source.

31. (original) The computer peripheral of claim 27, further comprising at least one voltage regulator electrically coupled with the current driven circuitry.

32. (original) The computer peripheral of claim 27, further comprising serial termination coupled with the communication path.

33. (original) An apparatus comprising:

means for serially transmitting and receiving signals complying with the standard Universal Serial Bus (USB) specification over a communication path; and

means for serially transmitting and receiving signals at a frequency higher than the signals complying with the standard USB specification over the communication path.

34. (original) The apparatus of claim 33, wherein the higher frequency signals have a rate of about 125 megabits per second.

35. (original) The apparatus of claim 33, wherein the signals complying with the standard USB specification have a rate of 1.5 megabits per second or 12 megabits per second.

36. (original) The apparatus of claim 33, further comprising means for configuring the apparatus to transmit and receive the higher frequency signals.

37. (original) The apparatus of claim 33, wherein the communication path comprises at least one of a cable and a pin.

38. (original) A method comprising:

coupling at least two transceivers with a communication path;

wherein the transceivers are capable of transmitting and receiving signals complying with the standard Universal Serial Bus (USB) specification over the communication path;

wherein at least one of the transceivers is capable of transmitting and receiving signals at a frequency higher than the signals complying with the standard USB specification over the communication path;

wherein at least one of the transceivers is capable of configuring itself to transmit and receive the higher frequency signals.

39. (original) The method of claim 38, wherein the higher frequency signals have a rate of at least 125 megabits per second.

40. (original) The method of claim 38, wherein the signals complying with the standard USB specification have a rate of 1.5 megabits per second or 12 megabits per second.

41. (original) The method of claim 38, wherein the communication path comprises a cable.